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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known

Application Number	10/813,226
Filing Date	March 30, 2004
First Named Inventor	Honary, Hooman
Art Unit	2183
Examiner Name	David A. Wiley

Sheet

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of

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Attorney Docket No: 80107.115US1

US PATENT DOCUMENTS

Examiner Initial *	Cite No	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/S.C./		BENINI, L. , et al., "System-Level Power Optimization: Techniques and Tools", ACM Transactions on Design Automation of Electronic Systems, 5(2), (Apr. 2000), 115-192 Pgs.	

EXAMINER

/Sai Ming Chan/

DATE CONSIDERED

06/18/2008

Substitute Disclosure Statement Form (PTO-1449)

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